

# FGW50N60H

**Discrete IGBT** 

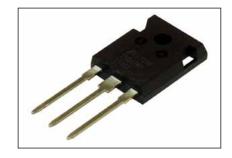
# Discrete IGBT (High-Speed V series) 600V / 50A

#### ■ Features

Low power loss Low switching surge and noise High reliability, high ruggedness (RBSOA, SCSOA etc.)

#### Applications

Uninterruptible power supply Power coditionner Power factor correction circuit

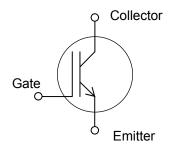


### ■ Maximum Ratings and Characteristics

# ● Absolute Maximum Ratings (at T<sub>c</sub>=25°C unless otherwise specified)

Items	Symbols	Characteristics	Units	Remarks
Collector-Emitter voltage	Vces	600	V	
Gate-Emitter voltage	V <sub>GES</sub>	±20	V	
DC Collector Current	I <sub>C@25</sub>	95	Α	Tc=25°C, Tj=150°C
	Ic@100	50	Α	Tc=100°C, Tj=150°C
Pulsed Collector Current	I <sub>CP</sub>	150	Α	Note *1
Turn-Off Safe Operating Area	-	150	Α	Vce≤600V, Tj≤175°C
Short Circuit Withstand Time	tsc	5	μs	Vcc≤300V, VcE=12V Tj≤150°C
Maximum Power Dissipation	P□	360	W	Tc=25°C
<b>Operating Junction Temperature</b>	T <sub>j</sub>	-40~+175	°C	
Storage Temperature	T <sub>stg</sub>	-55~+175	ç	

**■** Equivalent circuit



Note \*1 : Pulse width limited by Tjmax.

#### ● Electrical characteristics (at T<sub>j</sub>= 25°C unless otherwise specified)

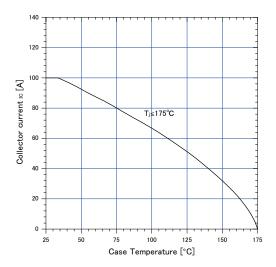
Items	Cumbala	Conditions		Characteristics			Unit	
	Symbols			min.	typ.	max.	Unit	
Collector-Emitter Breakdown Voltage	V <sub>(BR)CES</sub>	Ic = 250μA, V <sub>GE</sub> = 0V		600	-	-	V	
Zero Gate Voltage Collector Current	Ices	V <sub>CE</sub> = 600V, V <sub>GE</sub> = 0V	T <sub>j</sub> =25°C	-	-	250	μΑ	
	IGES		T <sub>i</sub> =175°C	-	-	10	mA	
Gate-Emitter Leakage Current	Iges	$V_{CE} = 0V$ , $V_{GE} = \pm 20V$		-	-	200	nA	
Gate-Emitter Threshold Voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = +20V, I <sub>C</sub> = 50mA		4.0	5.0	6.0	V	
Collector-Emitter Saturation Voltage	V <sub>CE</sub> (sat)	V <sub>GE</sub> = +15V, I <sub>C</sub> = 50A	T <sub>j</sub> =25°C	-	1.50	1.95	V	
			T <sub>j</sub> =175°C	-	1.80	-		
Input Capacitance	Cies	V <sub>CE</sub> =25V		-	4320	-	pF	
Output Capacitance	Coes	V <sub>GE</sub> =0V	V <sub>GE</sub> =0V		210	-		
Reverse Transfer Capacitance	Cres	f=1MHz	IMHz -	-	160	-		
Gate Charge		Vcc = 400V						
	Q <sub>G</sub>	Ic = 50A V <sub>GE</sub> = 15V		-	305	-	nC	
Turn-On Delay Time	t <sub>d(on)</sub>	T <sub>j</sub> = 25°C -		35	-			
Rise Time	t	Vcc = 400V	-	75	-	ns		
Turn-Off Delay Time	t <sub>d(off)</sub>	Ic = 50A	-	310	-			
Fall Time	tr	V <sub>GE</sub> = 15V - 60				-		
Turn-On Energy	Eon	R <sub>G</sub> = 10Ω		-	1.4	-		
		L = 500µH					mJ	
Turn-Off Energy	Eoff	Energy loss include "tail" a	Energy loss include "tail" and FWD		1.7	-	1113	
		(FDRW25S60L) reverse recovery.						
Turn-On Delay Time	t <sub>d(on)</sub>		$T_j = 175^{\circ}C$ - 40			-	ns	
Rise Time	t	Vcc = 400V	-	85	-			
Turn-Off Delay Time	t <sub>d(off)</sub>	Ic = 50A		-	335	-		
Fall Time	tr	V <sub>GE</sub> = 15V		-	72	-	7	
Turn-On Energy	Eon	R <sub>G</sub> = 10Ω		-	2.4	-		
		L = 500µH	L = 500µH				mJ	
Turn-Off Energy	Eoff	Energy loss include "tail" a		-	2.2	-	IIIJ	

## Thermal resistance

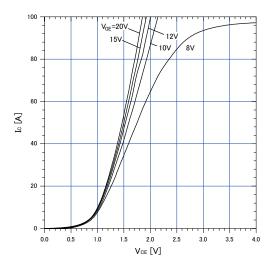
Items	Symbols	Conditions	Characteristics			Unit
			min.	typ.	max.	Unit
Thermal Resistance, Junction-Ambient	R <sub>th(j-a)</sub>		-	-	50	°C/W
Thermal Resistance Junction to Case	R <sub>th(i-c)</sub>		-	-	0.417	C/VV

# **■** Characteristics (Representative)

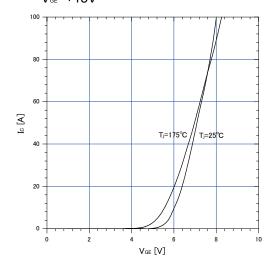
Graph.1 DC Collector Current vs  $T_c$   $V_{ce} \ge +15V$ ,  $T_i \le 175$ °C



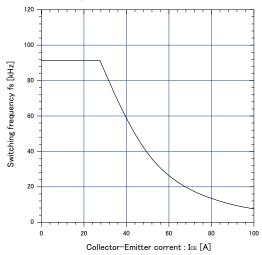
Graph.3 Typical Output Characteristics ( $V_{ce}$ - $I_c$ )  $T_j$ =25°C



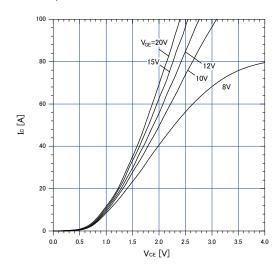
Graph.5
Typical Transfer Characteristics
V<sub>se</sub>=+15V



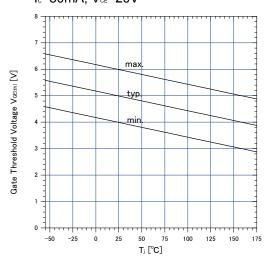
Graph.2 Collector Current vs. switching frequency  $V_{\text{ce}}$ =+15V,  $T_{\text{c}}$ ≤175°C,  $V_{\text{co}}$ =400V, D=0.5,  $R_{\text{e}}$ =10 $\Omega$ ,  $T_{\text{c}}$ =100°C



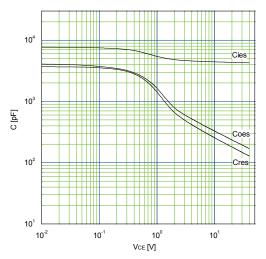
Graph.4 Typical Output Characteristics (V<sub>cE</sub>-I<sub>c</sub>) T<sub>i</sub>=175°C



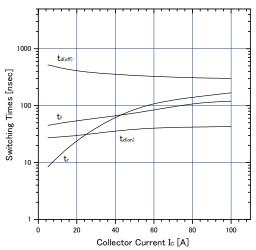
Graph.6
Gate Threshold Voltage vs. T<sub>i</sub>
I<sub>c</sub>=50mA, V<sub>cε</sub>=20V



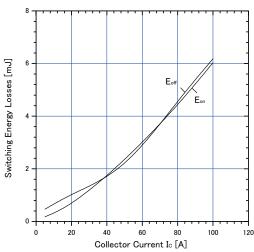
Graph.7 Typical Capacitance V<sub>c∈</sub>=0V, f=1MHz, T<sub>i</sub>=25°C



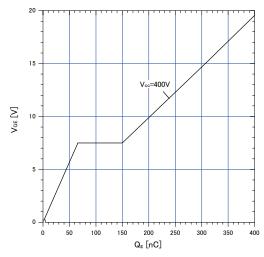
Graph.9 Typical switching time vs. Io T<sub>J</sub>=175°C, V<sub>CC</sub>=400V, L=500 $\mu$ H V<sub>GE</sub>=15V,R<sub>G</sub>=10 $\Omega$ 



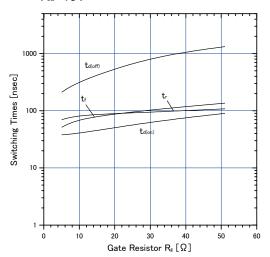
Graph.11 Typical switching losses vs.  $I_c$  T<sub>j</sub>=175°C,  $V_{cc}$ =400V, L=500 $\mu$ H  $V_{cE}$ =15V,  $R_c$ =10 $\Omega$ 



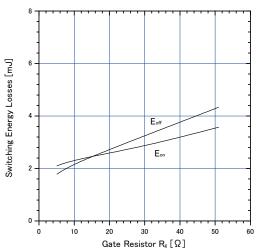
Graph.8 Typical Gate Charge V∞=400V, I₀=50A, T,=25°C



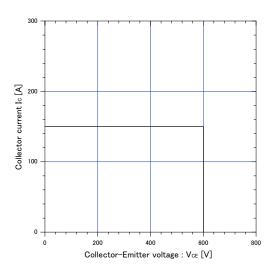
Graph.10 Typical switching time vs.  $R_{\rm s}$  T<sub>1</sub>=175°C,  $V_{\rm cc}$ =400V,  $I_{\rm c}$ =50A, L=500 $\mu$ H  $V_{\rm sc}$ =15V



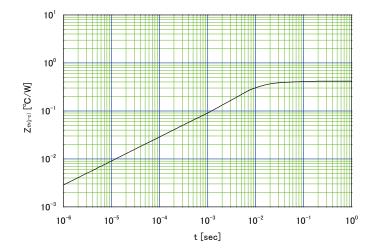
Graph.12 Typical switching losses vs.  $R_{\rm s}$  T<sub>1</sub>=175°C,  $V_{\rm cc}$ =400V,  $I_{\rm c}$ =50A, L=500 $\mu$ H  $V_{\rm se}$ =15V



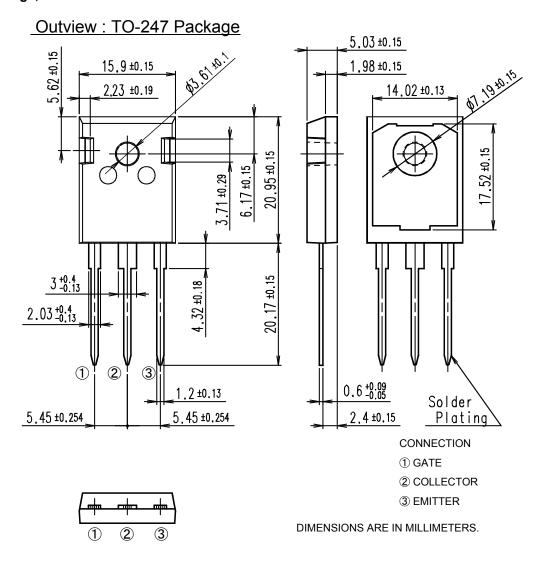
Graph.13 Reverse biased Safe Operating Area  $T_{\text{\tiny J}} \leq 175^{\circ}\text{C}, \ V_{\text{\tiny GE}} = +15\text{V/0V}, \ R_{\text{\tiny G}} = 10\Omega$ 



Graph.14 Transient thermal resistance of IGBT



# ■ Outline Drawings, mm



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