

6MBI100VB-120-50

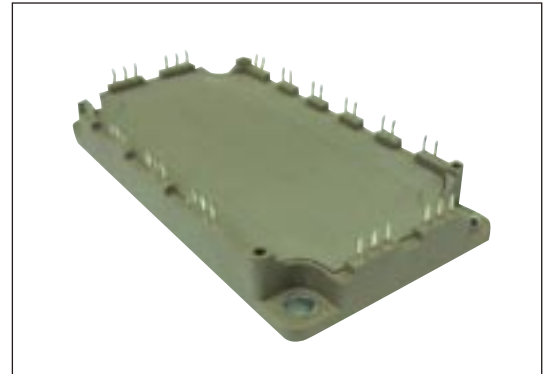
IGBT MODULE (V series) 1200V / 100A / 6 in one package

■ Features

- Compact Package
- P.C.Board Mount
- Low $V_{CE(sat)}$

■ Applications

- Inverter for Motor Drive
- AC and DC Servo Drive Amplifier
- Uninterruptible Power Supply
- Industrial machines, such as welding machines



■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at $T_c=25^\circ\text{C}$ unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings	Units
Inverter	Collector-Emitter voltage	V_{CES}			1200	V
	Gate-Emitter voltage	V_{GES}			± 20	V
	Collector current	I_c	Continuous	$T_c=80^\circ\text{C}$	100	A
		I_{cp}	1ms	$T_c=80^\circ\text{C}$	200	
		$-I_c$			100	
		$-I_c$ pulse	1ms		200	
Collector power dissipation	P_c	1 device		520	W	
Junction temperature	T_j			175	$^\circ\text{C}$	
Operating junction temperature (under switching conditions)	T_{jop}			150		
Case temperature	T_c			125		
Storage temperature	T_{stg}			-40 to +125		
Isolation voltage	between terminal and copper base (*1) between thermistor and others (*2)	V_{iso}	AC : 1min.			2500
Screw torque	Mounting (*3)	-	M5		3.5	N m

Note *1: All terminals should be connected together during the test.

Note *2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note *3: Recommendable value : 2.5-3.5 Nm (M5)

● Electrical characteristics (at Tj= 25°C unless otherwise specified)

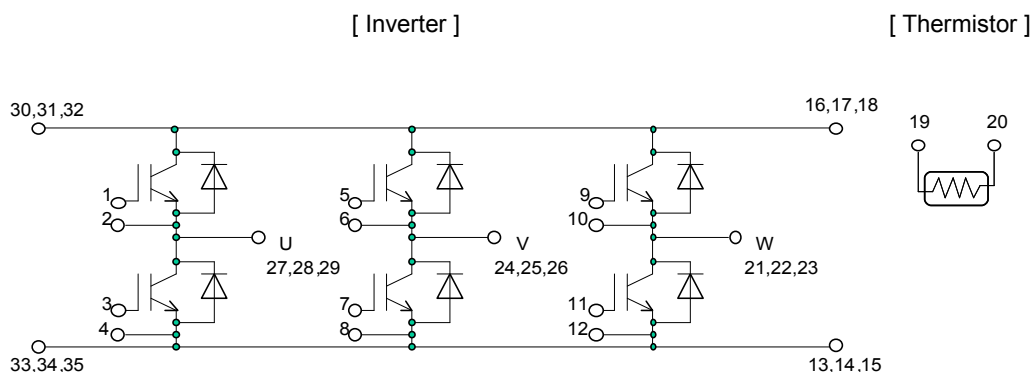
Items	Symbols	Conditions	Characteristics			Units	
			min.	typ.	max.		
Zero gate voltage collector current	I_{CES}	$V_{GE} = 0V, V_{CE} = 1200V$	-	-	1.0	mA	
Gate-Emitter leakage current	I_{GES}	$V_{GE} = 0V, V_{GE} = \pm 20V$	-	-	200	nA	
Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20V, I_c = 100mA$	6.0	6.5	7.0	V	
Collector-Emitter saturation voltage	$V_{CE(sat)}$ (terminal)	$V_{GE} = 20V$ $I_c = 100A$	Tj=25°C	-	2.30	2.75	V
			Tj=125°C	-	2.60	-	
			Tj=150°C	-	2.65	-	
	$V_{CE(sat)}$ (chip)	$V_{GE} = 15V$ $I_c = 100A$	Tj=25°C	-	1.75	2.20	
			Tj=125°C	-	2.05	-	
			Tj=150°C	-	2.10	-	
Input capacitance	C_{ies}	$V_{CE} = 10V, V_{GE} = 0V, f = 1MHz$	-	9.1	-	nF	
Turn-on time	t_{on}	$V_{CC} = 600V$ $I_c = 100A$ $V_{GE} = +15 / -15V$ $R_G = 1.6\Omega$	-	0.39	1.20	μs	
	t_r		-	0.09	0.60		
	$t_r(i)$		-	0.03	-		
Turn-off time	t_{off}	$R_G = 1.6\Omega$	-	0.53	1.00	μs	
	t_f		-	0.06	0.30		
Forward on voltage	V_F (terminal)	$I_F = 100A$	Tj=25°C	-	2.25	2.70	V
			Tj=125°C	-	2.40	-	
			Tj=150°C	-	2.35	-	
	V_F (chip)	$I_F = 100A$	Tj=25°C	-	1.70	2.15	
			Tj=125°C	-	1.85	-	
			Tj=150°C	-	1.80	-	
Reverse recovery time	t_{rr}	$I_F = \pm 20$	-	-	0.1	μs	
Resistance	R	T = 25°C	-	5000	-	Ω	
		T = 100°C	465	495	520		
B value	B	T = 25 / 50°C	3305	3375	3450	K	

● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance (1device)	Rth(j-c)	Inverter IGBT	-	-	0.29	°C/W
		Inverter FWD	-	-	0.44	
Contact thermal resistance (1device) (*4)	Rth(c-f)	with Thermal Compound	-	0.05	-	

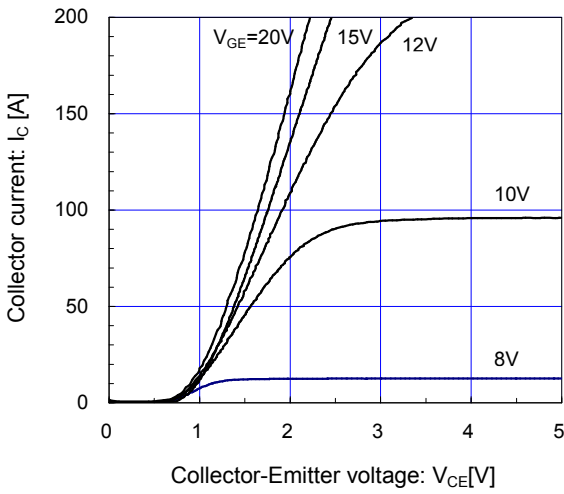
Note *4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

■ Equivalent Circuit Schematic

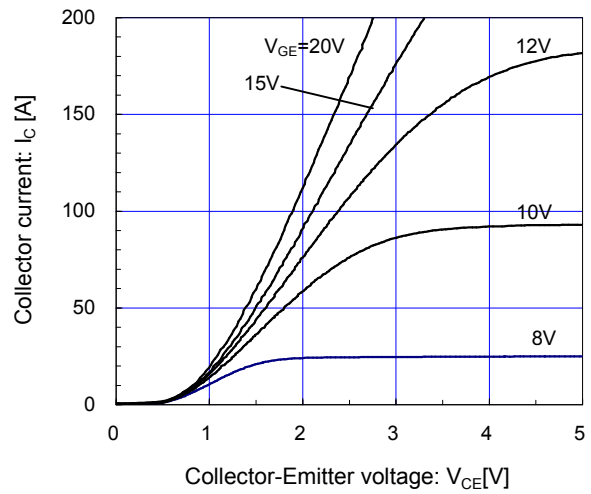


■ Characteristics (Representative)

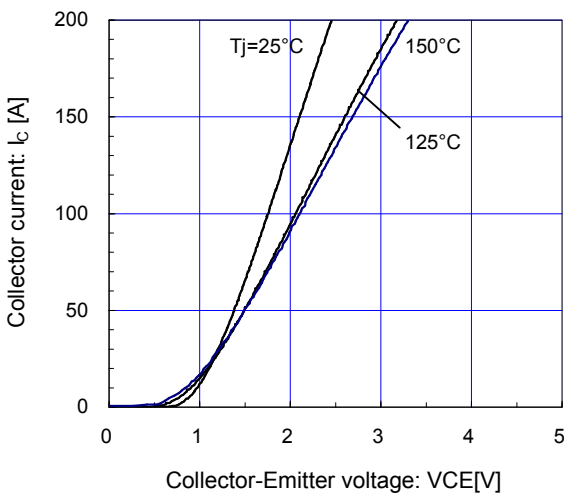
[Inverter]
 Collector current vs. Collector-Emitter voltage (typ.)
 $T_j = 25^\circ\text{C}$ / chip



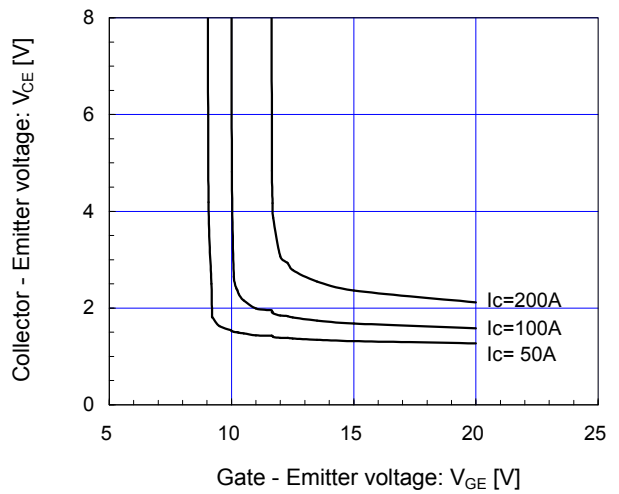
[Inverter]
 Collector current vs. Collector-Emitter voltage (typ.)
 $T_j = 150^\circ\text{C}$ / chip



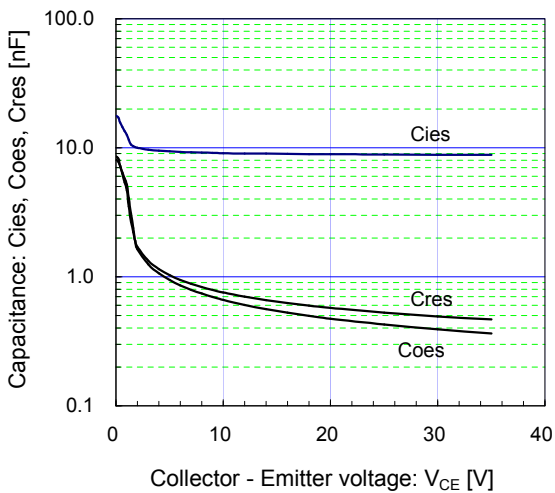
[Inverter]
 Collector current vs. Collector-Emitter voltage (typ.)
 $V_{GE} = 15\text{V}$ / chip



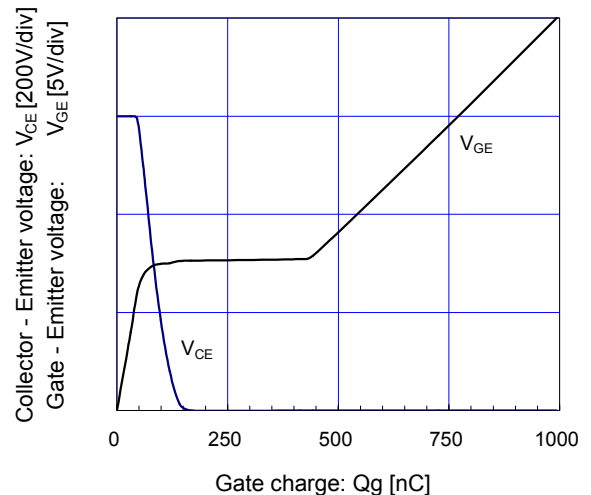
[Inverter]
 Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)
 $T_j = 25^\circ\text{C}$ / chip



[Inverter]
 Capacitance vs. Collector-Emitter voltage (typ.)
 $V_{GE} = 0\text{V}$, $f = 1\text{MHz}$, $T_j = 25^\circ\text{C}$

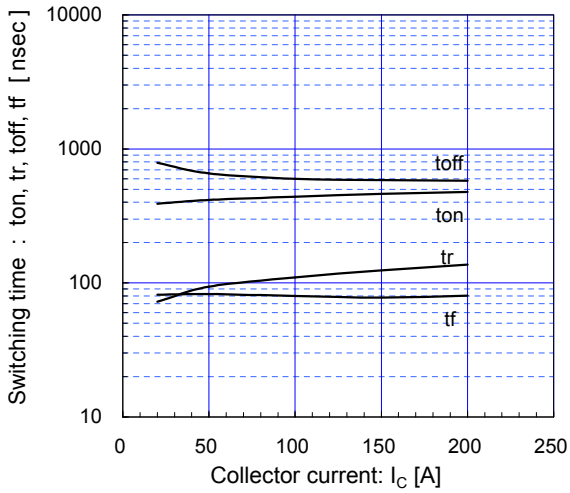


[Inverter]
 Dynamic gate charge (typ.)
 $V_{CC} = 600\text{V}$, $I_c = 100\text{A}$, $T_j = 25^\circ\text{C}$



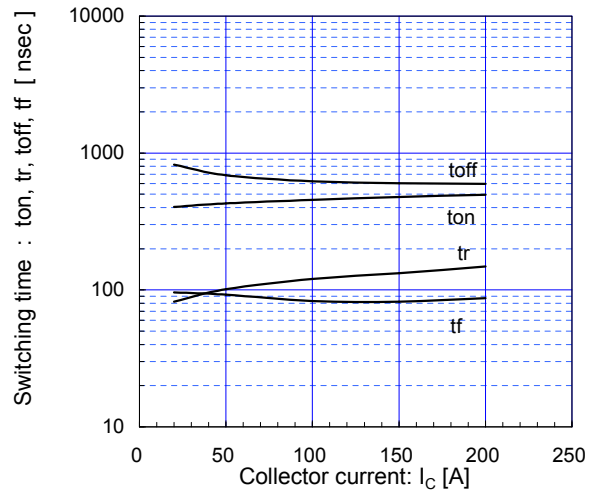
[Inverter]

Switching time vs. Collector current (typ.)
 $V_{cc}=600V, V_{GE}=\pm 15V, R_g=1.6\Omega, T_j=125^\circ C$



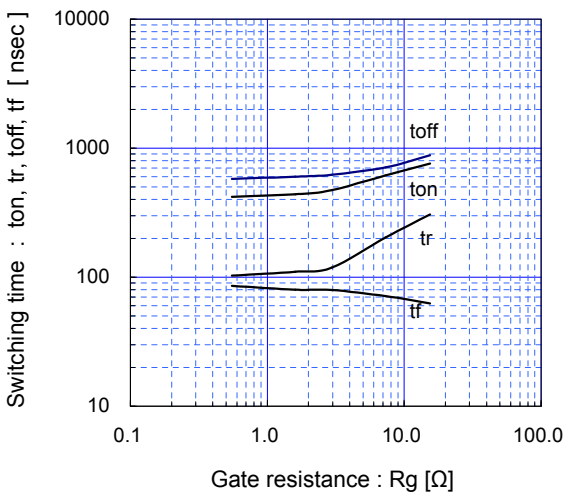
[Inverter]

Switching time vs. Collector current (typ.)
 $V_{cc}=600V, V_{GE}=\pm 15V, R_g=1.6\Omega, T_j=150^\circ C$



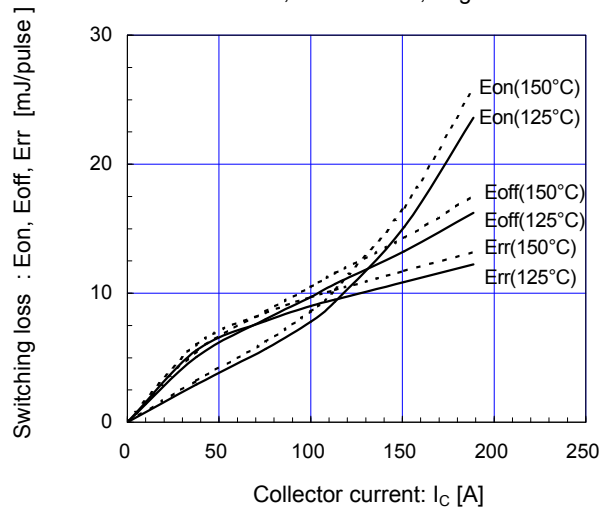
[Inverter]

Switching time vs. gate resistance (typ.)
 $V_{cc}=600V, I_c=100A, V_{GE}=\pm 15V, T_j=125^\circ C$



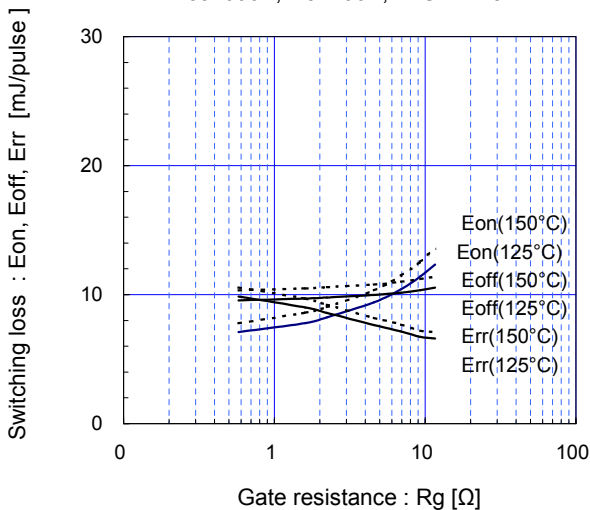
[Inverter]

Switching loss vs. Collector current (typ.)
 $V_{cc}=600V, V_{GE}=\pm 15V, R_g=1.6\Omega$



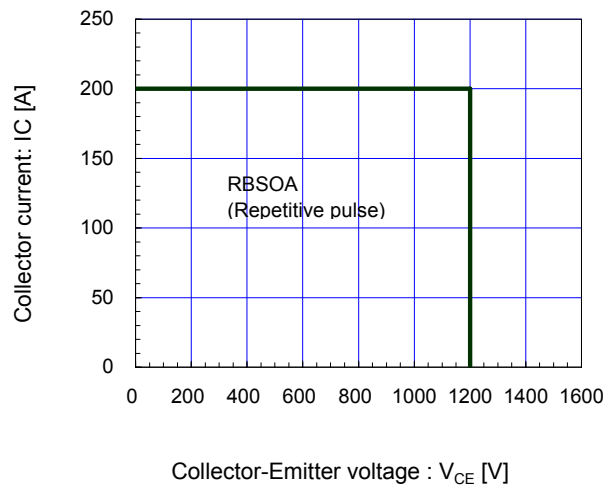
[Inverter]

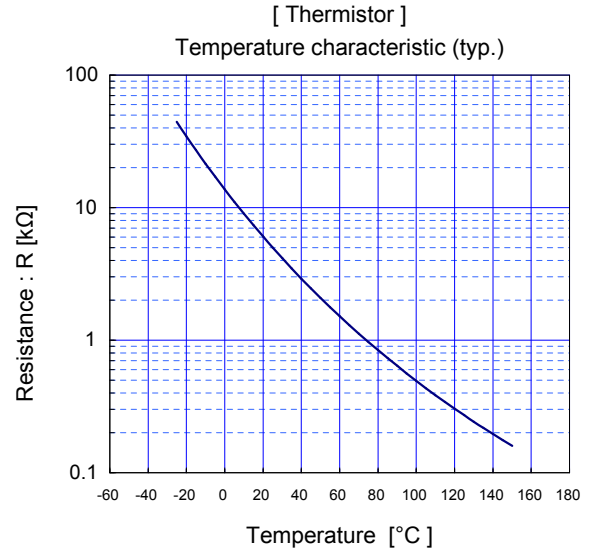
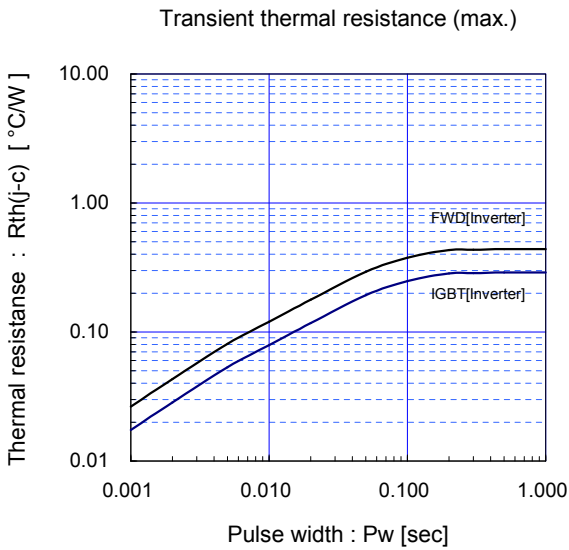
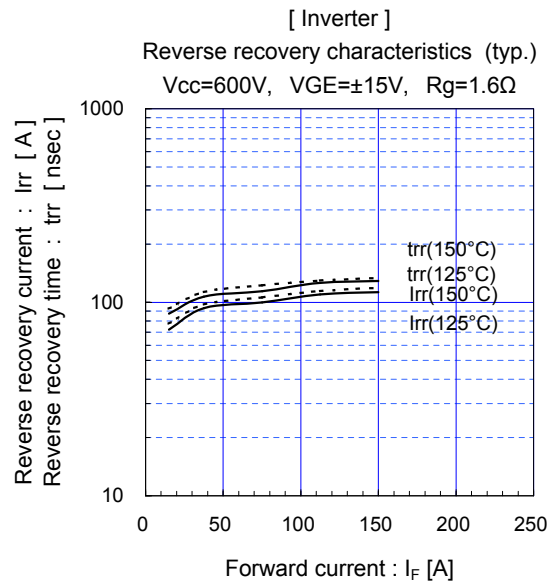
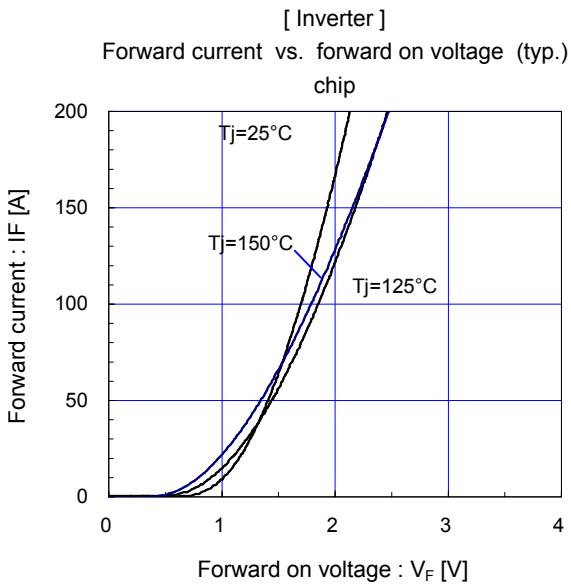
Switching loss vs. gate resistance (typ.)
 $V_{cc}=600V, I_c=100A, V_{GE}=\pm 15V$



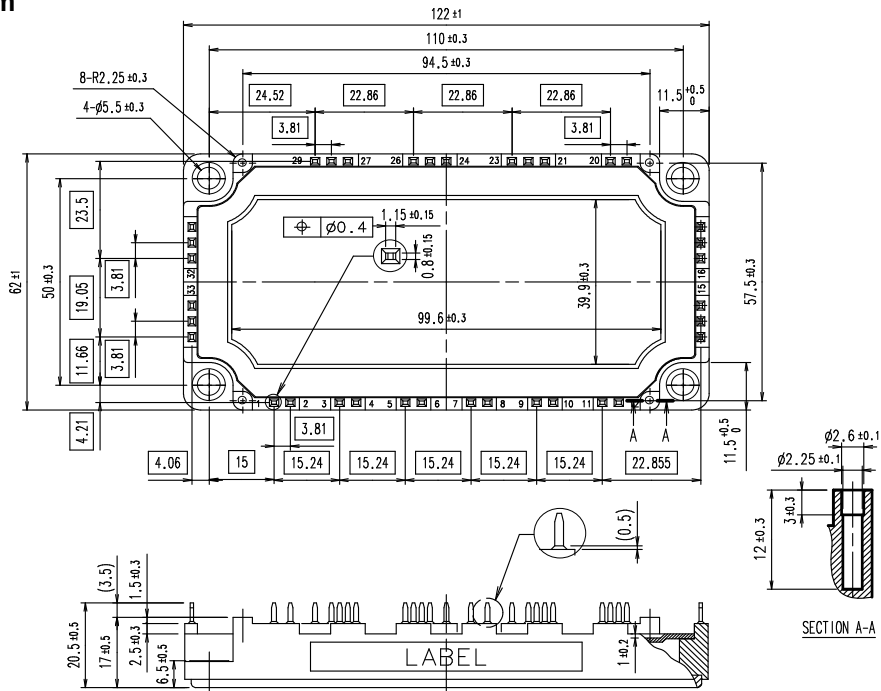
[Inverter]

Reverse bias safe operating area (max.)
 $+V_{GE}=15V, -V_{GE} \le 15V, R_g \ge 1.6\Omega, T_j \le 125^\circ C$





■ Outline Drawings, mm



WARNING

1. This Catalog contains the product specifications, characteristics, data, materials, and structures as of October 2008. The contents are subject to change without notice for specification changes or other reasons. When using a product listed in this Catalog, be sure to obtain the latest specifications.
2. All applications described in this Catalog exemplify the use of Fuji's products for your reference only. No right or license, either express or implied, under any patent, copyright, trade secret or other intellectual property right owned by Fuji Electric Device Technology Co., Ltd. is (or shall be deemed) granted. Fuji Electric Device Technology Co., Ltd. makes no representation or warranty, whether express or implied, relating to the infringement or alleged infringement of other's intellectual property rights which may arise from the use of the applications described herein.
3. Although Fuji Electric Device Technology Co., Ltd. is enhancing product quality and reliability, a small percentage of semiconductor products may become faulty. When using Fuji Electric semiconductor products in your equipment, you are requested to take adequate safety measures to prevent the equipment from causing a physical injury, fire, or other problem if any of the products become faulty. It is recommended to make your design fail-safe, flame retardant, and free of malfunction.
4. The products introduced in this Catalog are intended for use in the following electronic and electrical equipment which has normal reliability requirements.
 - Computers • OA equipment • Communications equipment (terminal devices) • Measurement equipment
 - Machine tools • Audiovisual equipment • Electrical home appliances • Personal equipment • Industrial robots etc.
5. If you need to use a product in this Catalog for equipment requiring higher reliability than normal, such as for the equipment listed below, it is imperative to contact Fuji Electric Device Technology Co., Ltd. to obtain prior approval. When using these products for such equipment, take adequate measures such as a backup system to prevent the equipment from malfunctioning even if a Fuji's product incorporated in the equipment becomes faulty.
 - Transportation equipment (mounted on cars and ships) • Trunk communications equipment
 - Traffic-signal control equipment • Gas leakage detectors with an auto-shut-off feature
 - Emergency equipment for responding to disasters and anti-burglary devices • Safety devices
 - Medical equipment
6. Do not use products in this Catalog for the equipment requiring strict reliability such as the following and equivalents to strategic equipment (without limitation).
 - Space equipment • Aeronautic equipment • Nuclear control equipment
 - Submarine repeater equipment
7. Copyright ©1996-2008 by Fuji Electric Device Technology Co., Ltd. All rights reserved.
No part of this Catalog may be reproduced in any form or by any means without the express permission of Fuji Electric Device Technology Co., Ltd.
8. If you have any question about any portion in this Catalog, ask Fuji Electric Device Technology Co., Ltd. or its sales agents before using the product.
Neither Fuji Electric Device Technology Co., Ltd. nor its agents shall be liable for any injury caused by any use of the products not in accordance with instructions set forth herein.