

# 2MBI225U4N-170-50

**IGBT Modules** 

# IGBT MODULE (U series) 1700V / 225A / 2 in one package

#### ■ Features

High speed switching Voltage drive Low Inductance module structure

## Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines



# ■ Maximum Ratings and Characteristics

#### ● Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

tems		Symbols	Conditions		Maximum ratings	Units	
Collector-Emitter voltage		V <sub>CES</sub>			1700	V	
Gate-Emitter voltage		V <sub>GES</sub>			±20	V	
Collector current		Ic	Continuous	Tc=25°C	300		
				Tc=80°C	225		
		Icp	1ms	Tc=25°C	600	^	
				Tc=80°C	450	Α	
		-lc	1ms		225		
		-lc pulse			450		
Collector power dissipation		Pc	1 device		1040	W	
Junction temperature		Tj			150	°C	
Storage temperature		Tstg			-40 to +125		
Isolation voltage	between terminal and copper base (*1)	.,	AC : 1min.		2400	VAC	
	between thermistor and others (*2)	V <sub>iso</sub>			3400		
Screw torque	Mounting (*3)				3.5	NI ma	
	Terminals (*4)	1-			4.5	N m	

Note \*1: All terminals should be connected together when isolation test will be done.

Note \*2: Two thermistor terminals should be connected together, each other terminals should be connected together and shorted to base plate when isolation test will be done.

Note \*3: Recommendable value : Mounting : 2.5-3.5 Nm (M5) Note \*4: Recommendable value : Terminals : 3.5-4.5 Nm (M6)

# ● Electrical characteristics (at Tj= 25°C unless otherwise specified)

14.		Cymholo	Canditions	O a malifi a ma		Characteristics		
Ш	ems	Symbols	Conditions		min.	typ.	max.	Units
Inverter	Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1700V		-	-	3.0	mA
	Gate-Emitter leakage current	Iges	$V_{CE} = 0V, V_{GE} = \pm 20V$		-	-	600	nA
	Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 225mA		4.5	6.5	8.5	V
	Collector-Emitter saturation voltage	V <sub>CE (sat)</sub>		Tj=25°C	-	2.60	2.85	V
		(terminal)	V <sub>GE</sub> = 15V	Tj=125°C	-	3.00	-	
		V <sub>CE (sat)</sub>	Ic = 225A	Tj=25°C	-	2.30	2.45	
		(chip)		Tj=125°C	-	2.65	-	
	Input capacitance	Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 1MHz		-	21	-	nF
	Turn-on time	ton	1/ 0001/	-	0.62	1.20	μs	
		tr	V <sub>cc</sub> = 900V I <sub>c</sub> = 225A		-	0.39		0.60
		tr (i)	- Ic = 225A - V <sub>GE</sub> = ±15V	-	0.05	-		
	Turn-off time	toff	$R_{\rm G} = 2.2\Omega$		-	0.55		1.50
		tf	RG - 2.202		-	0.09	0.30	
	Forward on voltage	VF		Tj=25°C	-	2.05	2.35	- V
		(terminal)	V <sub>GE</sub> = 0V	Tj=125°C	-	2.25	-	
		VF	I <sub>F</sub> = 225A	Tj=25°C	-	1.80	1.95	
		(chip)		Tj=125°C	-	2.00	-	
	Reverse recovery time	trr	$I_F = 225A$		-	0.18	0.6	μs
	Lead resistance, terminal-chip (*5)	R lead			-	1.30	-	mΩ
ţ	Resistance	R	T=25°C		-	5000	-	Ω
Thermistor	Resistance	T	T=100°C		465	495	520	
녙	B value	В	T=25/50°C		3305	3375	3450	K

Note \*5: Biggest internal terminal resistance among arm.

#### Thermal resistance characteristics

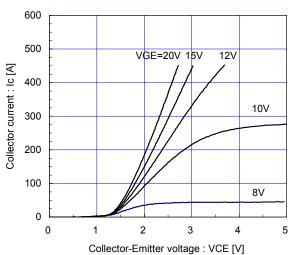
Items	Symbols	Conditions	Characteristics			Units
items		Conditions	min.	typ.	max.	Ullits
Thermal registance (4device)	Dth/i o)	IGBT	-	-	0.12	°C/W
Thermal resistance (1device)	Rth(j-c)	FWD	-	-	0.20	
ntact thermal resistance (1device) (*6) Rth(c-f)		with Thermal Compound	-	0.0167	-	

Note \*6: This is the value which is defined mounting on the additional cooling fin with thermal compound.

### ■ Characteristics (Representative)

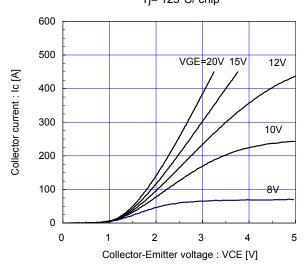
Collector current vs. Collector-Emitter voltage (typ.)

Tj= 25°C / chip

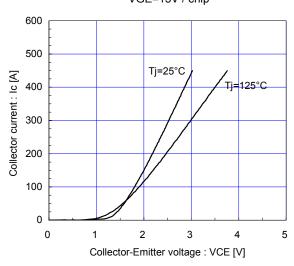


Collector current vs. Collector-Emitter voltage (typ.)

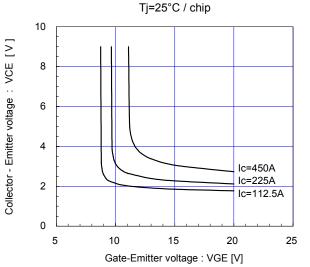
Tj= 125°C/ chip



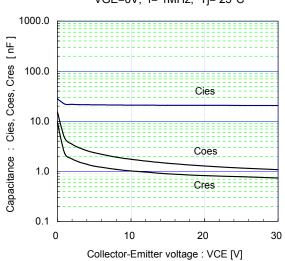
Collector current vs. Collector-Emitter voltage (typ.) VGE=15V / chip



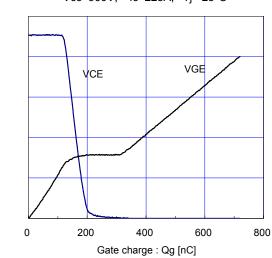
Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)



Capacitance vs. Collector-Emitter voltage (typ.) VGE=0V, f= 1MHz, Tj= 25°C

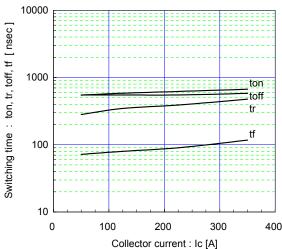


Dynamic Gate charge (typ.) Vcc=900V, Ic=225A, Tj= 25°C

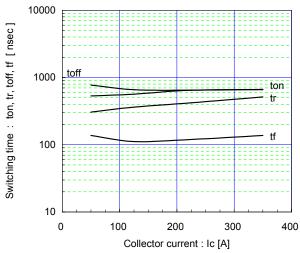


Collector-Emitter voltage: VCE [200V/div] Gate - Emitter voltage: VGE [5V/div]

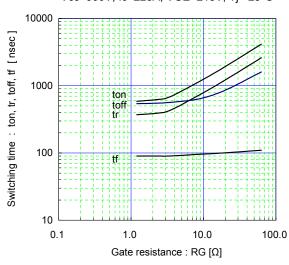
Switching time vs. Collector current (typ.) Vcc=900V, VGE= $\pm$ 15V, Rg=2.2 $\Omega$ , Tj= 25°C



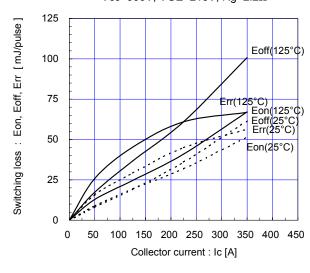
Switching time vs. Collector current (typ.) Vcc=900V, VGE= $\pm$ 15V, Rg=2.2 $\Omega$ , Tj=125°C



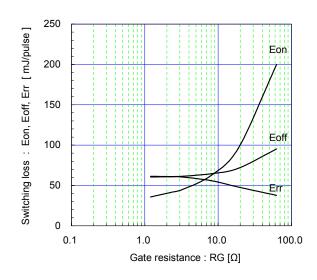
Switching time vs. Gate resistance (typ.) Vcc=900V, Ic=225A, VGE=±15V, Tj= 25°C



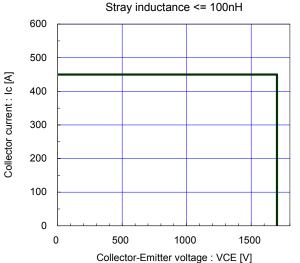
Switching loss vs. Collector current (typ.) Vcc=900V,  $VGE=\pm15V$ ,  $Rg=2.2\Omega$ 



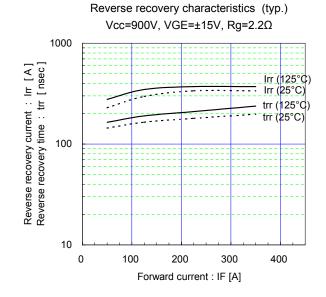
Switching loss vs. Gate resistance (typ.) Vcc=900V, Ic=225A, VGE=±15V, Tj= 125°C

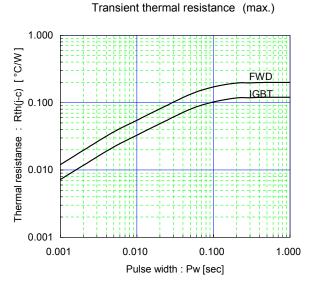


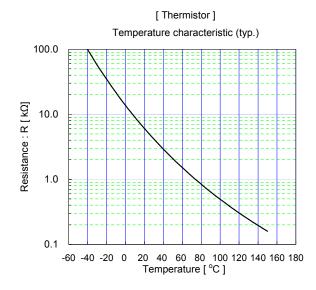
Reverse bias safe operating area (max.) +VGE=15V,-VGE <= 15V, RG >=  $2.2\Omega$ , Tj <=  $125^{\circ}$ C Stray inductance <= 100nH



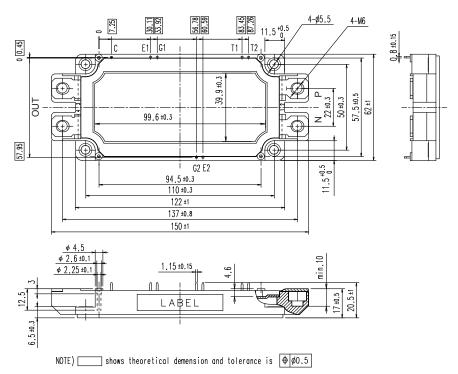
Forward current vs. Forward on voltage (typ.) chip 600 500 Tj=25°C Forward current : IF [A] 400 Ti=125°C 300 200 100 0 2 4 0 Forward on voltage : VF [V]



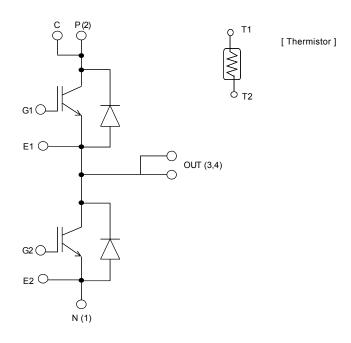




# ■ Outline Drawings, mm



# **■** Equivalent Circuit Schematic



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